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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re plication of

PATENT

Examiner:

Group Art Unit: 2123

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Serial No.: 09/853,026

Filed: May 11, 2001

PROCESS FOR INCREASING THE

EFFICIENCY OF A COMPUTER IN FINITE ELEMENT SIMULATIONS AND A COMPUTER FOR PERFORMING

THAT PROCESS

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

For:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, Applicants requests that the publication (copy enclosed), listed on the attached PTO-Form 1449, be made of record in the above-identified application.

Respectfully submitted,

Mark S. Bicks

Reg. No. 28,770

Roylance, Abrams, Berdo & Goodman, L.L.P. 1300 19th Street, N.W., Suite 600 Washington, D.C. 20036 (202) 659-9076

Datad.

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| Form PTO-1449 | | | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | | | | | | | | | ATTY. DOCKET NO. 41608 | | | | SERIAL NO. 09/853,206 | | | |
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*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw Line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.